



UNIVERSITI
TEKNOLOGI
MARA

Cawangan Kedah
Kampus Sungai Petani

ICRAIE

International Conference and Workshop on
Recent Advances and Innovation In Engineering **2019**

27-29 November 2019

Mercure Penang Beach,
Penang, Malaysia

Department of Computer Science, Universiti Teknologi MARA (UiTM) Kedah Branch, Kedah, Malaysia, in collaboration with Poornima College of Engineering, Rajasthan Jaipur, India and IEEE Chapters of Poland as well as Malaysia is honored to organize the 4th ICRAIE 2019 which will be held at Mercure Penang Beach, Penang, Malaysia on 27th and 28th of November 2019. Prospective authors from academia, as well as industry, are invited to submit their full papers that illustrate original / unpublished research, surveying works and industrial applications to ICRAIE 2019.



Professor Dr. Maria Ganhza
Warsaw University of Technology,
Poland

- Published closed to 200 research papers.
- On editorial boards of 5 journals and book series.
- Principal Investigator of the SRIPAS team in the INTER-IoT project.

- Director of the Cloud Computing and Distributed Systems (CLOUDS) Laboratory at the University of Melbourne, Australia.
- Founding CEO of Manjrasoft Pty Ltd., a spin-off company of the university, commercialising its innovations in Cloud Computing.
- Served as a Future Fellow of the Australian Research Council during 2012 until 2016.
- Highly cited authors in computer science and software engineering worldwide (h-index=123, g-index=271, and 79,000+ citations).
- Founding Editor-in-Chief (EIC) of IEEE Transactions on Cloud Computing (TCC).
- Recognized as a "Web of Science Highly Cited Researcher" for three consecutive years since 2016.
- Fellow of IEEE and Scopus Researcher of the Year 2017 with Excellence in Innovative Research Award by Elsevier for his outstanding contributions to cloud computing.

Professor Dr. Rajkumar Buyya
University of Melbourne,
Australia



Professor Paweł Gępcer
Intel Platform Architect,
France



- Has written 50 technical papers on Computer Science and Technology.
- Platform Architect focused on High Performance Computing.
- Joined Intel in 1996 as Field Application Engineer for Central and Eastern Europe.
- Responsible for driving Pentium III server project at IBM Development Center in Greenock.
- Intel Corporation spoke person responsible for communication with the press reg. technical and technology aspect of Intel's products and technology.



IMPORTANT DATES

Call for Papers	: 15 March 2019
Review Timeline	: 15 July 2019 until 14 August 2019
Notification of Acceptance Date	: 1 September 2019
Early Bird Registration Deadline	: 20 September 2019
Full Paper Submission Deadline	: 01 August 2019
Late Registration	: 15 November 2019
Final Submission for Camera Ready	: 20 Oktober 2019
Conference	: 27 – 28 November 2019
Post-Conference Workshop	: 29 November 2019

RESEARCH TRACKS

Track I (Computer Science / Information Technology)	Track II (Electronics & Communication Engineering)	Track III (Electrical Engineering)
Artificial Intelligence and Neural Networks	Wireless Communication & Networks	Renewable Energy Systems
Genetic Algorithms	Digital Electronics	Green Energy
Automated Software Engineering	Digital Signal Processing	Power System Stability & Control
Computer Networks	Microprocessor Based Technologies	Smart Grid
Advance Computer Architecture	Biomedical Engineering	Load Management
Computer and Mathematical Modeling	Remote Sensing and Satellite Communications	Wireless Power Transmission
Mobile and Pervasive Computing	Smart Antennas and Wave Propagation	Hybrid Electrical Vehicles
Optimization Techniques	Antenna Design	Power Quality
Computer Aided Design and Automation	Radar Technology	Control Systems
Robotics & Control Systems	Microwave Remote Sensing	Energy Efficiency & Audit
Fuzzy Systems and Soft Computing	Wireless Sensor Networks	Intelligent Machine & Control
Security and Cryptography	VLSI, VHDL & Chip Level Design	High Voltage AC and DC
Natural Language Processing	Image Processing & Applications	Generation Transmission & Distribution issues
Reconfigurable Computing	CAD Technology, and Simulation	Locomotives
Information and Knowledge Management	Modulation Techniques	Power Electronics Drives & Control
Parallel and Distributed Computing	Error Control Coding and Information Theory	Power System Control & Optimization
High Performance Computing	Optical Communications	Computer Algorithms
Data Mining & Warehousing	Real Time Embedded Systems	PLC & SCADA
Database Management Systems	Carbon Nanotubes & Applications	Power Line Communication Networks
Expert System & Its Application	Sensors for Intelligent Manufacturing	High Voltage Engineering
Multimedia Information Processing and Retrieval	Nanoelectronics and Quantum Computing	Electrical Machines
Computational Intelligence	Cognitive Radio Networks	Lightning Detection and Protection
Big Data and E-Science Applications	Industrial Electronics and Automations	Power Economic
Internet-of-Things (IoT)	Software Technology in Communication	Electrical Measurements
Software Defined Networks	Engineering Power Economic	Network & Coupled Circuits
ICT for Healthcare	System-On-Chip (SOC)	Micro-Grid Issues
Algorithms & Protocols	Signals and Systems	Fault Analysis & Protection
Ubiquitous Computing	Mechatronics	Insulation Technologies
Modern Database Theory And Implementation Techniques	Information Theory and Coding	Integration issues in Conventional and Renewable Power Generation
	Vehicular Electronics	

PAPER PUBLICATION

IEEE Xplore®
Digital Library

All accepted and presented papers will be submitted to IEEE for publication in IEEE Xplore Digital Library.

REGISTRATION AND SUBMISSION

For registration and submission, please visit our website
<https://icraie19.wixsite.com/icraie2019>

The full list can be viewed at our website <https://icraie19.wixsite.com/icraie2019>

FEES

Malaysian Delegates (RM)				
Category	Early Registration (Before 20 September 2019)		Normal Registration	
	IEEE Member	Non IEEE	IEEE Member	Non IEEE
Student	800	900	1300	1400
Academic	1300	1400	1400	1500
Industry	1700	1800	1800	1900

International Delegates (USD)				
Category	Early Registration (Before 20 September 2019)		Normal Registration	
	IEEE Member	Non IEEE	IEEE Member	Non IEEE
Student	200	250	300	400
Academic	300	400	400	500
Industry	400	500	500	600

PAYMENT METHOD

For Local Presenter/Participant

Academic Institutional cheques / Banker's cheques / Inter Bank Giro (IBG) / Bank Transfer / Government Local Order (LO). Personal Cheques are not accepted. All cheques must be made payable to "Bendahari UiTM Kedah".

For International Presenter/Participant

Telegraphic Transfers / Electronic Transfers ONLY (including bank charges).

PAYMENT INFORMATION

Bank Name:

Bank Islam Malaysia Bhd (Sungai Petani Branch)

Bank Address:

No. 71 & 72 Lorong Lagenda 1, Lagenda Heights, 08000 Sungai Petani, Kedah, Malaysia.

Bank Account Number:

02020010021120

Bank Account Name:

UiTM Cawangan Kedah

Pay to:

Bendahari UiTM Kedah

Swift Code:

BIMBMYKL

Proof of Payments

Please attach the scanned version of bank transaction slip or receipt together with the event details (either ICRAIE 2019/ Workshop) as a proof payment by email to 4thicraie2019@gmail.com. Please bring along your proof of payment on the registration day.

Cancellation and Refund Policy

Payments made are non-refundable

TECHNICAL SPONSORS:



IEEE
COMPUTER
SOCIETY



IEEE
Computational
Intelligence
Society

CO-ORGANIZER:



IEEE

IEEE IE/IA Malaysia Joint Chapter

HOSTED BY:



Department of
Computer Science
UiTM Kedah Branch,
Kedah, Malaysia



Poornima College
of Engineering

CONTACT US

ICRAIE 2019 Secretariat,
Department of Computer Science,
Universiti Teknologi MARA (UiTM)
Kedah Branch, 08400 Merbok,
Kedah, Malaysia.

Phone:

+604-456 2167/2462/2457

Fax:

+604-4562223

Email:

4thicraie2019@gmail.com

General Inquiry:

Dr Taniza Tajuddin

Phone: +604-4562167